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Assoc **...** Not vulnerable to interfering thread's **miss frequency ...**
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> use of **caches** with the **frequency** model is optional. **...** Example 3: Capturing
> effect of I-**cache** and D-**cache miss** (WCEC = 9 + 2N). Results **...**
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[PS] <u>Cache-Memory and TLB Calibration Tool Stefan Manegold 1 Peter ...</u>
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The **frequency** of **cache misses** depends on the access stride and the **cache** line size.
... Matching curves mean, that the **cache miss frequency** has reached its **...**
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